

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

108339-09057

SERIAL NO.

09/709.532

## LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

APPLICANT

BOWES

FILING DATE

November 13, 2000

GROUP

Not yet assigned

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
MM	AA	5,568,477	10/22/96	Galand et al			
	AB	5,696,899	12/09/97	Kalwitz			
	AC	5,790,539	08/04/98	Chao et al			
	AD	5,802,287	09/01/98	Rostoker et al			
	AE	5,825,772	10/20/98	Dobbins et al			
	AF	5,831,980	11/03/98	Varma et al			
	AG	5,845,081	12/01/98	Rangarajan et al			
	AH	5,887,187	03/23/99	Rostoker et al			
	AI	5,892,922	04/06/99	Lorenz			
	AJ	5,898,687	04/27/99	Harriman et al			
	AK	5,909,686	06/01/99	Muller et al			
	AL	5,918,074	06/29/99	Wright et al			
	AM	5,987,507	11/16/99	Creedon et al			
	AN	5,940,596	08/17/99	Rajan et al			
	AO	6,011,795	01/04/00	Varghese et al			
	AP	5,473,607	12/05/95	Hausman et al			
	AQ	5,787,084	07/28/98	Hoang et al			
Y	AR	5,423,015	06/06/95	Chung			

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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION		
							YES	NO	PART.
MM	AS	WO99/00944	01/07/99	WIPO			xx		
	AT	WO99/00948	01/07/99	WIPO			xx		
	AU	WO99/00949	01/07/99	WIPO			xx		
	AV	WO99/00939	01/07/99	WIPO			xx		
	AW	WO99/00950	01/07/99	WIPO			xx		
	AX	WO99/00938	01/07/99	WIPO			xx		
↓	AY	WO99/00945	01/07/99	WIPO			xx		

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## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

	AZ	
	BA	
	BB	

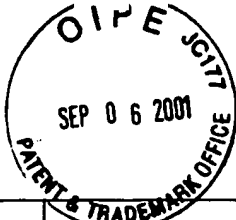
EXAMINER  <i>Michael J. Mone, Jr.</i>	DATE CONSIDERED  1/13/04
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		FILING DATE November 13, 2000	GROUP

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Mm	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
	AJ	6,175,902 B1	Jan. 16, 2001	Runaldue et al.			Dec. 18, 1997
	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
	AM	5,845,081	Dec. 1, 1998	Rangarajan et al.			Sep. 3, 1996
	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
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	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
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	AV	6,041,053	Mar. 21, 2000	Douceur et al.			Sep. 18, 1997
	AW	5,459,717	Oct. 17, 1995	Mullan et al.			Mar. 25, 1994
	AX	5,909,686	Jun. 1, 1999	Muller et al.			Jun. 30, 1997
	AY	5,887,187	Mar. 23, 1999	Rostoker et al.			Apr. 14, 1997
	AZ	5,568,477	Oct. 22, 1996	Galand et al.			Jun. 27, 1995
	BA	5,414,704	May 9, 1995	Spinney			Apr. 5, 1994
	BB	5,390,173	Feb. 14, 1995	Spinney et al.			Oct. 22, 1992
	BC	5,825,772	Oct. 20, 1998	Dobbins et al.			Apr. 2, 1996
	BD	5,790,539	Aug. 4, 1998	Chao et al.			Jan. 29, 1996



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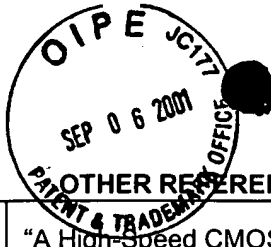
Sheet 2 of 3

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
MM	BE	5,918,074	Jun 29, 1999	Wright et al.			Jul. 25, 1997
	BF	5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
	BG	5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
	BH	5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
	BI	5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
	BJ	5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
	BK	5,579,301	Nov. 26, 1996	Ganson et al.			Feb. 28, 1994
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	BQ	EP0859492A2	Aug. 19, 1998	EPO					
	BR	EP0312917A2	Apr. 26, 1989	EPO					
	BS	EP0853441A2	Jul. 15, 1998	EPO					
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	CA	WO 99/00949	Jan. 7, 1999	WIPO					
	CB	WO 99/00950	Jan. 7, 1999	WIPO					
	CC	WO 99/00939	Jan. 7, 1999	WIPO					
	CD	WO 99/00938	Jan. 7, 1999	WIPO					
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	CF	WO 99/00945	Jan. 7, 1999	WIPO					
	CG	EP0849917A2	Jun. 24, 1998	EPO					
	CH	EP0907300A2	Apr. 7, 1999	EPO					



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MM	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
MM	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
MM	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondo et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
MM	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
MM	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

EXAMINER <i>Michael J. Moore, Jr.</i>	DATE CONSIDERED <i>11/13/04</i>
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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
MM	AA	5,781,745	07/14/98	Ramelson et al			
	AB						
	AC						
	AD						
	AE						
	AF						

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							YES	NO	PART.
	AG								
	AH								
	AI								
	AJ								
	AK								
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## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

MM	AM	Anonymous: "Request/Reply Loss Detection", Research Disclosure, Vol. 41, No. 416, December 1, 1998, XP002178210
MM	AN	Macleod K, "Enhanced Flow Control for 802.3 Links Utilizing Extensions of 802.3x Flow Control", IEEE 802 QOS/Flow Control Study Group Albuquerque Plenary Interim Meetings, November 1998, XP002178211
	AO	

EXAMINER

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